

What is claimed is:

1. A display apparatus, comprising:
a plurality of gate lines provided in one direction of a
substrate;
a plurality of drain lines provided in a direction
intersecting with said gate lines; and
a plurality of display pixels, each of which is selected by
a scan signal supplied from corresponding one of said plurality
of gate lines, and which is supplied with an image signal from
corresponding one of said plurality of drain lines; wherein
each of said plurality of display pixels comprises:
a display element;
a storing circuit for storing a digital image signal from said
corresponding one of said plurality of drain lines in response to
a scan signal from said corresponding one of said gate lines; and
a signal selector for selecting a signal for display based
on the digital signal stored at said storing circuit and for
supplying said selected signal to said display element.

2. A display apparatus of claim 1, wherein
said storing circuit comprises a predetermined number of
storing elements, said number corresponding to the number of bits
in said digital image signal; and
said signal selector selects a signal to be supplied to said

display element from among a predetermined number of signals, said number corresponding to the number of bits in said digital image signal.

5 3. A display apparatus of claim 1, wherein,
said storing circuit stores said digital image signal using one or more inverters.

10 4. A display apparatus of claim 1, wherein
said storing circuit stores said digital image signal using one or more inverters and a capacitor.

15 5. A display apparatus of claim 1 wherein,
said plurality of display pixels is capable of displaying a still image.

20 6. A display apparatus of claim 1, wherein,
after a still image is written to each of said plurality of display pixels as a digital image signal, operations of driving circuits for driving said plurality of display pixels are stopped until a new digital image signal is written to the same display pixels.

25 7. A display apparatus of claim 1, wherein,
said display apparatus is a liquid crystal display apparatus;

and

said display element includes a liquid crystal capacitor and a pair of electrodes for driving said liquid crystal capacitor.

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8. A display apparatus of claim 7, wherein,

said pair of electrodes for driving said liquid crystal capacitor comprises an individual display electrode for each display pixel and a facing electrode provided to face said display electrode; and

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at least one of the signals selected by said signal selector is an alternating current voltage signal which oscillates around the voltage of said facing electrode.

9. A display apparatus, comprising:

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a plurality of gate lines provided in one direction of a substrate;

a plurality of drain lines provided in a direction intersecting with said gate lines; and

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a plurality of display pixels, each of which is selected by a scan signal supplied from corresponding one of said plurality of gate lines, and which is supplied with an image signal from corresponding one of said plurality of drain lines; wherein

each of said plurality of display pixels comprises:

a display element;

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a first display circuit having a storing circuit, for storing

a digital image signal from said corresponding one of drain lines
in response to a scan signal from said corresponding one of gate
lines, and a signal selector for selecting a signal for display
based on the digital signal stored in said storing circuit and
5 supplying said selected signal to said display element; and

10 a second display circuit having a storage capacitor for
storing an analog image signal from said corresponding one of drain
lines in response to a scan signal from said corresponding one of
gate lines, wherein the signal stored in said storage capacitor
is supplied to said display element.

10. A display apparatus of claim 9, wherein

15 said display pixel further comprises a display circuit
selector for selectively supplying an image signal from said
corresponding one of drain lines to said first or second display
circuit.

11. A display apparatus of claim 9, wherein

20 said corresponding one of drain lines is constructed from a
line for digital image signals and a line for analog image signals,
and

said first display circuit is connected to said line for
digital image signals and said second display circuit is connected
to said line for analog image signals.

12. A display apparatus of claim 9, wherein,
said display pixel further comprises a data selector for
selectively supplying an output signal from said first or second
display circuit to said display element.

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13. A display apparatus of claim 9, wherein
said storing circuit comprises a predetermined number of
storing elements, said number corresponding to the number of bits
in said digital image signal; and

10 said signal selector selects a signal to be supplied to said
display element from among a predetermined number of signals, said
number corresponding to the number of bits in said digital image
signal.

15 14. A display apparatus of claim 9, wherein,
said storing circuit stores said digital image signal using
one or more inverters.

20 15. A display apparatus of claim 9, wherein
said storing circuit stores said digital image signal using
one or more inverters and a capacitor.

25 16. A display apparatus of claim 9, wherein,
said plurality of display pixels is capable of displaying a
still image.

17. A display apparatus of claim 9, wherein,
after a still image is written to each of said plurality of
display pixels as a digital image signal, operations of driving
circuits for driving said plurality of display pixels are stopped
5 until a new digital image signal is written to the same display
pixels.

18. A display apparatus of claim 9, wherein,
said display apparatus is a liquid crystal display apparatus;
10 and
said display element includes a liquid crystal capacitor and
a pair of electrodes for driving said liquid crystal capacitor.

19. A display apparatus of claim 18, wherein,
15 said pair of electrodes for driving said liquid crystal
capacitor comprises an individual display electrode for each
display pixel and a counter electrode provided to face said display
electrode; and

at least one of the signals selected by said signal selector
20 is an alternating current voltage signal which oscillates around
the voltage of said counter electrode.

20. A display apparatus, comprising:
a plurality of gate lines provided in one direction of a
25 substrate;

a plurality of drain lines provided in a direction intersecting with said gate lines; and

a plurality of display pixels, each of which is selected by a scan signal supplied from corresponding one of said plurality of gate lines, and which is supplied with an image signal from corresponding one of said plurality of drain lines; wherein

each of said plurality of display pixels comprises:

a display element;

10 a first display circuit having a storing circuit, for storing a digital image signal from said corresponding one of said plurality of drain lines in response to a scan signal from said corresponding one of said plurality of gate lines; and

15 a second display circuit having a storage capacitor for storing an analog image signal from said corresponding one of said plurality of drain lines in response to a scan signal from said corresponding one of said plurality of gate lines.

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